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843.37558VX1

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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/902,673	07/12/2001	Michimasa Funabashi	843.37558VX1	3110

TITLE OF INVENTION: METHOD OF MANUFACTURING A SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE

APPLN. TYPE	SMALL ENTITY	ISSUE FEE	PUBLICATION FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	NO	\$1330	\$300	\$1630	06/29/2004

EXAMINER	ART UNIT	CLASS-SUBCLASS
MALSAWMA, LALRINFAMKIM HMAR	2825	438-745000

1. Change of correspondence address or indication of "Fee Address" (37 CFR 1.363).

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Antonelli, Terry, Stout
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(B) RESIDENCE: (CITY and STATE OR COUNTRY)

Renesas Technology Corp.

Tokyo, Japan

Please check the appropriate assignee category or categories (will not be printed on the patent); ☐ individual ☒ corporation or other private group entity ☐ government

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William I. Solomon # 28,565

June 29, 2004

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01 FC:1501
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